

REASONS FOR ALLOWANCE

Allowable Subject Matter

1. Claims 1-17 are allowed over the prior art.
2. The following is a statement of reasons for the indication of allowable subject matter:

The prior art of record either singularly or in combination fails to teach the clock synchronization of IEEE 1394 devices connected on different hardwired buses that attempt communication over 802.11a/b wireless local area network wherein a software beacon alert (SBA) is received by a wireless master device attached to a first bus, associating said master device with a first phase-lock loop circuit that uses a phase detector with an asymmetrical gain about zero error, applying said SBA to said first phase-lock loop circuit, transmitting a timing message from said master device to a second phase-lock loop circuit associated with at least one slave device attached to a second bus and providing said timing message from said second phase-lock loop circuit to said at least one slave device before said master device receives a subsequent SBA.

Aiello et al (US Patent Publication No. 2002/0018458 A1) discloses clock synchronization in IEEE 1394 devices that communicate using 802.11 standards wherein Aiello discloses transmitting a timing message from a master device to at least one slave device, said at least one slave device containing a phase-lock loop circuit. However, Aiello et al fails to disclose said master and slave devices being connected to different buses, said master device receiving an SBA, applying said SBA to a phase-lock loop circuit that uses a phase detector with an asymmetrical gain about zero error and providing said timing message to said at least one slave device before said master device receives a subsequent SBA.

Straub et al (US Patent No. 6,914,895 B1) discloses clock synchronization in IEEE 1394 devices connected on different hardwired buses wherein Straub discloses transmitting a timing message from a master device to at least one slave device, said at

least one slave device containing a phase-lock loop circuit. However, Straub et al fails to disclose said master device receiving an SBA, applying said SBA to a phase-lock loop circuit that uses a phase detector with an asymmetrical gain about zero error and providing said timing message to said at least one slave device before said master device receives a subsequent SBA.

Shpak (US Patent Publication No. 2003/0207697 A1) discloses clock synchronization between devices that attempt communication over an 802.11 wireless local area network wherein Shpak discloses transmitting a timing message from a master device to at least one slave device. However, Shpak fails to disclose said master and slave devices being connected to different buses, said master device receiving an SBA, applying said SBA to a phase-lock loop circuit that uses a phase detector with an asymmetrical gain about zero error and providing said timing message to said at least one slave device associated with a phase-lock loop circuit before said master device receives a subsequent SBA.

The prior art of record fails to teach claimed limitations of a master device receiving an SBA, applying said SBA to a phase-lock loop circuit that uses a phase detector with an asymmetrical gain about zero error and providing said timing message to said at least one slave device before said master device receives a subsequent SBA. The prior art of record either singularly or in combination fails to teach the above claimed limitations for claims 1, 6 and 12 and are therefore the reason for allowance. Claims 2-5 are dependent on claim 1 and are therefore allowed under the same reasons set forth above. Claims 7-11 are dependent on claim 6 and are therefore allowed under the same reasons set forth above. Claims 13-17 are dependent on claim 12 and are therefore allowed under the same reasons set forth above.

Conclusion

3. The prior art not relied upon but considered pertinent to applicant's disclosure is made of record and listed on form PTO-892.

Any inquiry concerning this communication or earlier communications from the examiner should be directed to TANGELA T. CHAMBERS whose telephone number is 571-270-3168. The examiner can normally be reached Monday through Thursday, 9:00am-6:30pm Eastern Time.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Nick Corsaro, can be reached at 571-272-7876. The fax phone number for the organization where this application or proceeding is assigned is 571-270-4168.

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